



**PATENT**

**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Warren M. Farnworth

**Serial No.:** 09/548,942

**Filed:** April 13, 2000

**For:** METHOD AND APPARATUS OF  
INTERPOSING VOLTAGE REFERENCE  
TRACES BETWEEN SIGNAL TRACES IN  
SEMICONDUCTOR DEVICES

**Confirmation No.:** 6934

**Examiner:** B. Lee

**Group Art Unit:** 2817

**Attorney Docket No.:** 2269-4161US  
(98-1265.00/US)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

August 22, 2006  
Date

Signature

Tim Palfreyman  
Name (Type/Print)

*Approved for Entry*

**AMENDMENT UNDER 37 C.F.R. §1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Final Office Action mailed June 22, 2006, the three-month shortened statutory period for response to which expires on September 22, 2006. This response is submitted on or before two months from the mailing date of the Final Office Action.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.